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James J. Dade	(Depositor's name)
<i>[Signature]</i>	(Signature)
September 13, 2005	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/015,209	11/16/2001	Eiki Hashimoto	NEKU 19.181	5984

TITLE OF INVENTION: SEMICONDUCTOR CIRCUIT DESIGNING APPARATUS AND A SEMICONDUCTOR CIRCUIT DESIGNING METHOD IN WHICH THE NUMBER OF STEPS IN A CIRCUIT DESIGN AND A LAYOUT DESIGN IS REDUCED

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	09/27/2005

EXAMINER	ART UNIT	CLASS-SUBCLASS
THOMPSON, ANNETTE M	2825	716-001000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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Katten Muchin

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

NEC Electronics Corporation

Kawasaki, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.
- ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(e)(2).

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Date

9/12/05

Typed or printed name

Brian F. Hennessey

Registration No.

51,271

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